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jc715 U.S. PTO

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A

PATENT APPLICATION
COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, D.C. 20231

Express Mail No. EL662347785US
Attorney Docket No. 00-103

Transmitted herewith for filing is the patent application of:
Inventor : Margaret S. Fyfield
For : Probing Fixture and Method of Semiconductor Wafer

Enclosed are:

- ☒ Specification, Claims and Abstract.
- ☒ 4 Sheets of Drawings.
- ☒ Combined Declaration and Power of Attorney (signed).
- ☒ Assignment and Recordation Form Cover Sheet.
- ☒ Request and Certification under 35 U.S.C. 122(b)(2)(B)(i)

The filing fee has been calculated as shown below:

(Col. 1)			SMALL ENTITY		OTHER THAN A SMALL ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE	OR	RATE FEE
BASIC FEE	//////////	//////////	/////	\$355	OR	///// \$710
TOTAL CLAIMS	*20 - 20 =	*0	X 9 =	\$	OR	X 18 = \$
INDEP CLAIMS	*2 - 3 =	*0	x 40 =	\$	OR	x 80 = \$
MULTIPLE DEPENDENT CLAIM PRESENTED			+135 =	\$	OR	+270 = \$
*If the difference in Col. 1 is less than zero, enter "0" in Col. 2			TOTAL \$		OR	TOTAL \$710.00

- ☐ Please charge my Deposit Account No. _____ in the amount of \$_____. A duplicate of this sheet is attached.
- ☒ A check in the amount of \$750.00 is enclosed to cover the filing fee (\$710.00 for filing fee and \$40.00 for the assignment fee).
- ☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 12-1087. A duplicate copy of this sheet is enclosed.
 - ☒ Any patent application processing fees under 37 C.F.R. 1.17.
 - ☐ The issue fee set in 37 C.F.R. 1.18 at or before mailing of the Notice of Allowance, pursuant to 37 C.F.R. 1.311(b).
 - ☒ Any filing fees under 37 C.F.R. 1.16 for presentation of extra claim.

Dated at Englewood, Colorado, this 6th day of December, 2000.

Respectfully submitted,

John R. Ley
Registration No. 27,453
ATTORNEY FOR APPLICANT

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09/731596
12/06/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Margaret S. Fyfield)
Serial No.)
Filed: Concurrently Herewith)
For: Probing Fixture and Method for)
Semiconductor Wafer)
_____)

CERTIFICATE OF MAILING BY EXPRESS MAIL

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

The undersigned hereby certifies that the attached Transmittal Letter; Specification, Claims and Abstract; Four (4) Sheets of Drawings; Combined Declaration and Power of Attorney (signed); Assignment and Recordation Form Cover Sheet; and Request and Certification under 35 U.S.C. 122(b)(2)(B)(i); check in the amount of \$750.00 (\$710.00 for filing fee and \$40.00 for the assignment fee); Certificate of Mailing by Express Mail; and Return Card, relating to the above application, were deposited as "Express Mail," Mailing Label No. EL662347785US, with the United States Postal Service, addressed to The Commissioner of Patents and Trademarks, Washington, D.C. 20231, on this 6th day of December, 2000.

12/6/00

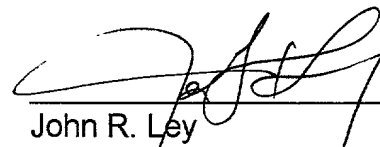
Date



Mailer

12/6/00

Date



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

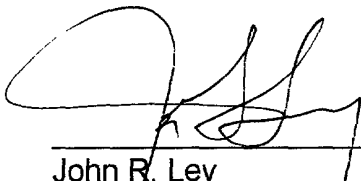
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Semiconductor Wafer)
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REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)

Based on information and instructions from the Assignee of the above identified and concurrently filed application, it is hereby certified that the invention disclosed in the application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. It is hereby requested that the concurrently filed application not be published under 35 U.S.C. 122(b).

12/6/00

Date



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